



05-09-03

2813

Please type a plus sign (+) inside this box → ☐PTO/SB/21 (6-98)
Approved for use through 09/30/2000 OMB 0651-0031
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/388,826
	Filing Date	September 1, 1999
	First Named Inventor	Weimin Li, et al.
	Group Art Unit	2813
	Examiner Name	E. Kielin
Total Number of Pages in This Submission	Attorney Docket Number	MI22-1208

ENCLOSURES (check all that apply)		
<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Assignment Papers (for an Application)	<input type="checkbox"/> After Allowance Communication to Group
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
<input type="checkbox"/> Amendment / Response	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
<input type="checkbox"/> After Final	<input type="checkbox"/> Petition Routing Slip (PTO/SB/69) and Accompanying Petition	<input type="checkbox"/> Proprietary Information
<input type="checkbox"/> Affidavits/declaration(s)	<input type="checkbox"/> Petition to Convert to a Provisional Application	<input type="checkbox"/> Status Letter
<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address	<input checked="" type="checkbox"/> Additional Enclosure(s) (please identify below)
<input type="checkbox"/> Express Abandonment Request	<input type="checkbox"/> Terminal Disclaimer	PTO Return Postcard, Form 1449; and cited art.
<input checked="" type="checkbox"/> Supplemental Information Disclosure Statement	<input type="checkbox"/> Small Entity Statement	
<input type="checkbox"/> Certified Copy of Priority Document(s)	<input type="checkbox"/> Request for Refund	
<input type="checkbox"/> Response to Missing Parts/Incomplete Application	Remarks	
<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	D. Brent Kenady; Wells, St. John, PS
Signature	
Date	5-7-03

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on this date: <input type="text"/>		
Typed or printed name	<input type="text"/>	
Signature	<input type="text"/>	Date

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

EV085430309



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/388,826
Filing Date September 1, 1999
Inventor Weimin (Michael) Li et al.
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner E. Kielin
Attorney's Docket No. MI22-1208
Title: Low k Interlevel Dielectric Layer Fabrication Methods

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -See Attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the references which are listed on the attached Form PTO-1449, copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of the referenced art is respectfully requested.

This Supplemental Information Disclosure Statement is being filed after the filing of the Request for Continued Examination (RCE) Application and before receipt of the first Office Action. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Respectfully submitted,

Dated: 5-7-03

By: D. Brent Kenady
D. Brent Kenady
Reg. No. 40,045

Form PTO/SB-100 MAY 07 2001 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M22-1208		SERIAL NO. 09-388,826		
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Weimer, Michael J. et al.		
				FILING DATE September 1, 1999		
				GROUP 2813		
U.S. PATENT DOCUMENTS						
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA 6,087,064	07/91	Lin, et al.			
	AB					
	AC					
	AD					
	AE					
	AF					
	AG					
	AH					
	AI					
	AI					
FOREIGN PATENT DOCUMENTS						
	Document Number	Date	Country	Class	Subclass	Translation
						Yes No
	AK					
	AL					
	AM					
	AN					
	AO					
	AP					
	AQ					
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)						
	AR		Wolf et al., "Silicon Processing for the VLSI Era - Vol. 1 - Process Technology," Plenum, page viii (2 pages total).			
	AS					
	AT					
EXAMINER			DATE CONSIDERED			
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.						

ENCLOSURE